

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of:

Jean-Paul CLAVEQUIN, et al.

Serial No.: TBA

Filed: November 6, 2003

For:      SYNCHRONIZED COMMUNICATION  
                BETWEEN INTEGRATED CIRCUIT  
                CHIPS

)  
)  
)) Group Art Unit: TBA  
)  
)) Examiner: TBA  
)  
)) Attorney Docket No. 003921.00149  
)  
)

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to Applicant's duty of good faith and candor as set forth in 37 C.F.R. § 1.56(a) Applicant submits herewith the reference cited on the attached PTO form 1449. Copies of the cited reference are attached. Applicant respectfully requests that the Examiner consider these references in connection with the search of the prior art required by 37 C.F.R. § 1.104.

Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed documents as a competent reference against the claims of the present application.

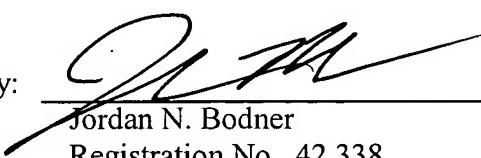
It is respectfully requested that the Examiner fully consider each item of information, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the information has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Jean-Paul CLAVEQUIN, et al.  
Attorney Docket No.: 003921.00149

It is believed that no fee is required to ensure consideration of the cited references by the Examiner. However, if a fee is deemed necessary, the Commissioner is authorized to charge our Deposit Account No. 19-0733. A duplicate copy of this sheet is enclosed for accounting purposes.

Respectfully submitted,

BANNER & WITCOFF, LTD.

By:   
Jordan N. Bodner  
Registration No. 42,338

Eleventh Floor  
1001 G Street, N.W.  
Washington, D.C. 20001-4597  
(202) 824-3000

Dated: November 6, 2003

|   |   |                          |                     |                        |              |
|---|---|--------------------------|---------------------|------------------------|--------------|
| Substitute for form 1449A/PTO   |   | <b>Complete if Known</b> |                     |                        |              |
| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br><br>(use as many sheets as necessary) |   | Application Number       | TBA                 |                        |              |
|   |   | Filing Date              | November 6, 2003    |                        |              |
|   |   | First Named Inventor     | Jean-Paul CLAVEQUIN |                        |              |
|   |   | Group Art Unit           | TBA                 |                        |              |
|   |   | Examiner Name            | TBA                 |                        |              |
| Sheet   | 1 | of                       | 1                   | Attorney Docket Number | 003921.00149 |

| <b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b> |                       |   |  |  |                |
|---|-----------------------|---|--|--|----------------|
| Examiner Initials *                                       | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |  |  | T <sup>2</sup> |
|   | A                     | "Data to Clock Phase Alignment", Nick SAWYER, Xilinx Application Note, April 4, 2002.   |  |  |                |
|   | B                     |   |  |  |                |
|   | C                     |   |  |  |                |
|   | D                     |   |  |  |                |
|   | E                     |   |  |  |                |
|   | F                     |   |  |  |                |
|   | G                     |   |  |  |                |
|   | H                     |   |  |  |                |
|   | I                     |   |  |  |                |
|   | J                     |   |  |  |                |
|   | K                     |   |  |  |                |

|                    |  |                 |  |
|--------------------|--|-----------------|--|
| Examiner Signature |  | Date Considered |  |
|--------------------|--|-----------------|--|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.